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INDIRA GANDHI DELHI TECHNICAL UNIVERSITY FOR WOMEN
(Established by Govt. of Delhi vide Act 9 of 2012)
CONSOLIDATED STATEMENT OF MARKS

Master of Technology (VLSI Design)



NAME: Khushboo Tyagi
ENROLLMENT: 014020172015
FATHER'S NAME: Kapil Kumar Tyagi
YEAR OF ADMISSION: 2015

TOTAL CREDITS OF PROGRAMME: 104
MINIMUM CREDITS REQUIRED: 100
YEAR OF COMPLETION: July 2017
PROGRAMME DURATION: 4 Semesters

PAPER CODE	PAPER NAME	INT	EXT	TOTAL	CS	PAPER CODE	PAPER NAME	INT	EXT	TOTAL	CS
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FIRST SEMESTER

MVD 501	CMOS Analog Circuit Design	31	50	81	4	MVD 503	Digital Integrated Circuits	35	50	85	4
MVD 505	Hardware Description Languages	33	54	87	4	MVD 507	Semiconductor Devices	32	45	77	4
MVD 509	Advanced IC Processing	33	38	71	4	MVD 511	CMOS Analog Circuit Design Lab	28	47	75	1
MVD 513	Digital Integrated Circuits Lab	32	47	79	1	MVD 515	Hardware Description Languages Lab	34	48	82	1
MVD 517	Technical Report Writing	-	-	74	2						

SECOND SEMESTER

MVD 502	Device Modeling & Circuit Simulation	28	47	75	4	MVD 504	VLSI Subsystem Design	34	48	82	4
MVD 506	Integrated Circuits for Analog Signal Processing	34	43	77	4	MVD 510	Deep Submicron CMOS ICs	28	44	72	4
MVD 522	MEMs & Microsystems	31	49	80	4	MVD 524	Device Modeling & Circuit Simulation Lab	36	50	86	1
MVD 526	VLSI Subsystem Design Lab	35	36	71	1	MVD 528	Lab based on Electives	34	42	76	1
MVD 530	Term Paper	-	-	83	2						

THIRD SEMESTER

MVD 601	Low Power VLSI Design	32	37	69	4	MVD 603	ASIC Design	29	51	80	4
MVD 605	VLSI Interconnects	31	48	79	4	MVD 613	Low Power VLSI Design Lab	32	45	77	1
MVD 615	ASIC Design Lab	35	53	88	1	MVD 617	Minor Project	29	34	63	12

FOURTH SEMESTER

MVD 602	Dissertation	28	46	74	24	MVD 604	Seminar and Progress Report	-	-	86	4
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CREDITS EARNED : 104 CREDITS ACCOUNTED FOR CPI : 100 CPI : 76.08 DIVISION : FIRST DIVISION WITH DISTINCTION

Date of Print: 08.09.2017

AB: Absent

INT: Internal

EXT: External

CS: Credit Secured

402/57
Dean (Examination Affairs)



LA
Registrar