



SI No. : 764

V.B.S. Purvanchal University, Jaunpur

FACULTY OF ENGINEERING & TECHNOLOGY

MARKS SHEET OF B.TECH. (4th YEAR 8th Semester), EXAMINATION - 2014

BRANCH: Electronics & Communication Engg.

NAME OF THE CANDIDATE: KM VAISHALI SINGH

ROLL-NO: 70311

FATHER'S/HUSBAND'S NAME: AJAY SINGH

ENROL-NO: PU-05/8530

INSTITUTE/COLLEGE: U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY, V.B.S.P.U. JAUNPUR

SUBJECT	MAX MARKS		MARKS OBTAINED		TOTAL
	Exam	SESS	EXAM	SESS	
1 Microelectronics & VLSI Design	100	50	50	38	88
2 Radar & Television Engg.	100	50	50	35	85
3 Open Elective -I (Microwave Communication)	100	50	23	39	62
4 Open Elective -II (Active Network) Practical	100	50	54	33	87
5 Microelectronics/CAD Lab	70	30	50	26	76
6 Major Project	300	100	248	75	323
7 Optical Communication	70	30	50	22	72

GRAND TOTAL Seven Hundred Ninety Three 1200 793

RESULT: Pass66.08%

DATE: 28/07/2014

CHECKED BY: _____


CONTROLLER OF EXAMS