

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

**CONSOLIDATED MARKS MEMO - CREDIT SHEET**

B.Tech. ELECTRONICS & COMMUNICATION ENGINEERING

CRM No. **c 0636480**

Serial No. 21130000558

Name : **NADEEM AKHTAR**

Roll Total No. 11ED1A0426



Name of the College : EC-N I E T, DESHMUKH

Month & Year of Final Exam : May, 2015

Class Awarded: **FIRST CLASS**

S.No	SUBJECT TITLE	WT marks	TUT marks	TOTAL	CREDITS	S.No	SUBJECT TITLE	WT marks	TUT marks	TOTAL	CREDITS
<b>I YEAR</b>											
1	ENGLISH	15	56	71	4	2	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	16	33	51	4
3	IT WORKSHOP & ENGINEERING WORKSHOP	23	45	68	4	4	MATHEMATICS - I	19	26	45	6
5	MATHEMATICAL METHODS	20	39	59	6	6	ENGINEERING PHYSICS	14	26	40	4
7	ENGINEERING CHEMISTRY	16	26	42	4	8	COMPUTER PROGRAMMING & DATA STRUCTURES	17	32	49	6
9	ENGINEERING DRAWING	20	26	46	4	10	COMPUTER PROGRAMMING LAB	21	48	69	4
11	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	22	42	64	4						
<b>I SEMESTER</b>						<b>II SEMESTER</b>					
1	MATHEMATICS-II	20	26	46	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	20	26	46	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	14	26	40	3	2	ELECTRONIC CIRCUIT ANALYSIS	22	32	54	4
3	ENVIRONMENTAL STUDIES	16	32	50	3	3	PULSE & DIGITAL CIRCUITS	21	26	47	4
4	ELECTRIC CIRCUITS	19	36	58	4	4	SWITCHING THEORY & LOGIC DESIGN	22	29	51	4
5	ELECTRONIC DEVICES & CIRCUITS	14	26	42	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	17	36	53	4
6	SIGNALS & SYSTEMS	16	27	43	4	6	ELECTRICAL ENGINEERING LAB	20	45	65	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	24	43	67	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	22	48	70	2
8	BASIC SIMULATION LAB	20	40	60	2	8	PULSE & DIGITAL CIRCUITS LAB	23	46	69	2
<b>I SEMESTER</b>						<b>II SEMESTER</b>					
1	COMPUTER ORGANIZATION	17	35	52	4	1	DIGITAL COMMUNICATIONS	22	38	50	3
2	CONTROL SYSTEMS	20	49	69	4	2	MICROPROCESSORS & MICROCONTROLLERS	22	38	50	4
3	ANTENNAS & WAVE PROPAGATION	22	32	54	3	3	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	24	26	50	4
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	22	34	56	4	4	OPERATING SYSTEMS	22	28	50	4
5	ANALOG COMMUNICATIONS	24	46	70	3	5	ADVANCED ENGLISH COMMUNICATION SKILLS LAB	16	47	63	2
6	IC APPLICATIONS	24	30	54	3	6	MICROPROCESSORS & MICROCONTROLLERS LAB	23	45	68	2
7	ANALOG COMMUNICATIONS LAB	25	49	74	2	7	DIGITAL SIGNAL PROCESSING LAB	22	48	68	2
8	IC APPLICATIONS LAB	24	48	73	2	8	DIGITAL SIGNAL PROCESSING	21	60	81	4
<b>I SEMESTER</b>						<b>II SEMESTER</b>					
1	EMBEDDED SYSTEMS	21	28	49	4	1	CELLULAR & MOBILE COMMUNICATIONS	20	36	56	3
2	MANAGEMENT SCIENCE	24	26	50	3	2	WIRELESS COMMUNICATIONS & NETWORKS	22	38	60	3
3	COMPUTER NETWORKS	24	26	50	4	3	INDUSTRY ORIENTED MINI PROJECT	-	45	45	2
4	TELECOMMUNICATION SWITCHING SYSTEMS	24	28	52	3	4	SEMINAR	46	-	46	2
5	VLSI DESIGN	25	27	52	4	5	MAJOR PROJECT	48	140	188	10
6	MICROWAVE ENGINEERING	23	37	60	3	6	COMPREHENSIVE VIVA	-	95	95	2
7	MICROWAVE ENGG. AND DIGITAL COMM. LAB	20	45	65	2	7	RADAR SYSTEMS	21	35	56	3
8	E-CAD AND VLSI LAB	22	50	72	2						

Number of Credits registered for : 200 Aggregate Marks Secured for best 200

Aggregate Marks Secured 3365 OUT OF 5200 (64.71%)

Date of issue: August 25, 2015

(see attached for Rules concerned to award of class)

\* indicates 'ABSENT'

(\*Classes registered but not counted for calculation of aggregate)



*(Signature)*

**CONTROLLER OF EXAMINATIONS**