



SL No. M14 0052382

Date: 03/07/2014

STATEMENT OF MARKS

B.TECH IN ELECTRONICS & TELECOMMUNICATION ENGINEERING  
EIGHTH SEMESTER EXAMINATION - FEBRUARY 2014

Name: VIJENDRA KUMAR Reg. No: 111/107/L/BTE10054

Paper Code	Paper / Course Title	Credits	Term-End Examination		Internal Assessment		Total			Result / Remark
			Max Marks	Marks Obt.	Max Marks	Marks Obt.	Max Marks	Min For Pass	Marks Obt.	
BEET5	VLSI - Technology & Process Modeling	5	75	53	25	24	100	40	77	PASS
BEET7	Satellite Communication Systems	3	75	55	25	23	100	40	78	PASS
BEET8	Semiconductor Devices and Modeling	5	75	61	25	22	100	40	83	PASS
BEET9	Project	5	75	56	25	23	100	40	79	PASS
<b>Grand Total</b>		<b>18</b>					<b>400</b>		<b>317</b>	

Total Credits Earned /										Grand Total	Percentage	Quality Level	Class
Semester	I Sem	II Sem	III Sem	IV Sem	V Sem	VI Sem	VII Sem	VIII Sem					
Total			544	513	552	528	477	317	2921	75.15	Excellent	Distinction	

Grand Total in Words: TWO THOUSAND NINE HUNDRED THIRTY-ONE

- Note:
1. Minima for pass - 30 marks per course in theory and aggregate 40% including internal assessment.
  2. Course Exemption - 40 marks and above in each course.
  3. Class Distinction - 75% & above; First Class - 60% & above but less than 74.99%; Second Class - 50% & above but less than 59.99%; Third Class - 40% & above but less than 49.99%; Fail - Below 40%

Entered by  
Institute of Engineering & Management

Verified by  
Signature  
Name: LOKESHA H C

Scrutinized by  
Signature  
Name: SIDDARAJU

