PROFILE

RAGHVENDRA SINGH



Objective: Seeking a position to utilize my skills and abilities in a technically growing environment that offers Professional growth while being resourceful, innovative and flexible.

Educational Qualifications:

- Perusing PhD (Electronics & Communication) from Rama University, Kanpur (2019).
- Post Graduation in Masters in Technology (VLSI Design) from University (2011-2013) with 91.14%.

Book:

• Low Power High Speed CMOS Adder, Evince pub Publishing, ISBN- 978-93-90197-10

Strong Technical Skills:

- Verilog and VHDL using Xilinx
- Downloading using FPGA, CPLD kit.
- CMOS Circuit Design Using (Virtuoso) Cadence
- Digital Circuit Design Using (SOC Encounter) Cadence
- CMOS Circuit Design Using (Aether) Empyrean
- Basic MATLAB Programming
- C programming

Award & Recognition:

- Young Scientist Award MPCST-Bhopal, 2012
- Young Scientist Award MPCST-Bhopal, 2013
- Chancellor Scholarship for 1st position in PG by ITM University, Gwalior.
- Gold Medal for Ist position in M. Tech Ist year (2011-2012).
- Active SPOC NPTEL Local Chapter-2019,2020

Workshop/Project:

M.Tech Thesis Projects:

"An Evolution of Low Power Full Adder for Nanoscale Using Power Gating Technique" Under the Guidance Dr. Shyam Akashe, Associate Professor, ITM University, Gwalior.

Professional Experience:

- Currently working as **Assistant Professor**, University Polytechnic (Diploma)Rama University Kanpur from July'2021 to till date.
- Currently working as Assistant Professor, Rama University Kanpur from August 2015 to June'2021date.

 Working as Trainee project on "CMOS Interfacing Circuit and Signal Conditioning for MEMS Gyroscope Device" under the guidance Dr. S. C Bose Scientist F, and Mr. G Rajahari, Scientist CSIR-CEERI, Pilani from July 2013 to 30 May 2014.

Personal Profile:

Father's Name : Mr. Munna Singh Date of Birth : 25 February,1985

Nationality : Indian Marital Status : Single

Languages Known : English & Hindi Coordinates : +91-9889439038

Email : <u>raghav.raghvendra7@gmail.com</u>

Address : Q No. 15, 111/97, Ashok Nagar, Kanpur -208012

Declaration:

I hereby declare that the information given above is correct to the best of my knowledge.

Date Warm Regard

Place Raghvendra Singh