

09/No. 185093

ਪੰਜਾਬ ਟੈਕਨੀਕਲ ਯੂਨੀਵਰਸਿਟੀ

**PUNJAB TECHNICAL UNIVERSITY**

Name of the College/Institute

C-Block, Mohali



Roll No./Regn. No.

81012141014

**RESULT-CUM-DETAILED MARKS CARD**

**Master of Technology ( VLSI DESIGN ) Full Time, December-2009**

Name : **Dashyant Kumar**

Father's Name : **Soran Singh**

Sl. No.	Paper	Subjects		Marks Obtained	Min. Pass Marks	Max. Marks
1.	VL-511	Semiconductor Devices	Theory	22	20	50
			Sessional	20	20	20
2.	VL-512	VLSI Design Concepts	Theory	24	20	50
			Sessional	20	20	20
3.	VL-513	Hardware Description Languages	Theory	28	20	50
			Sessional	20	20	20
4.	VL-514	VLSI Technology	Theory	30	20	50
			Sessional	20	20	20
5.	VL-515	Design of Analog/Mixed Mode VLSI Circuits	Theory	16	20	50
			Sessional	22	20	20
Lab	VL-515	VLSI Design-Lab	Practical	20	20	50
			Sessional	21	20	20
Lab	VL-517	Hardware Description Languages-Lab	Practical	32	20	50
			Sessional	16	20	20
6.	VL-521	Advanced Digital Signal Processing	Theory	28	20	50
			Sessional	20	20	20
7.	VL-522	ASIC Design/FPGA	Theory	34	20	50
			Sessional	21	20	20
8.	VL-523	Memory Design and Testing	Theory	33	20	50
			Sessional	16	20	20
9.	VL-524	Embedded Systems	Theory	28	20	50
			Sessional	18	20	20
10.	VL-525	Testing and Fault Tolerance	Theory	28	20	50
			Sessional	16	20	20
Lab	VL-525	D.S.P. and Embedded Systems-Lab	Practical	18	20	50
			Sessional	40	20	20
Lab	VL-527	Designing with FPGAs-Lab	Practical	40	20	50
			Sessional	16	20	20
11.	VL-531	System on Chip (SOC)	Theory	31	20	50
		(Elective-I)	Sessional	20	20	20
12.	VL-532	RF Design	Theory	30	20	50
		(Elective-II)	Sessional	27	20	20
	VL-533	Thesis Seminar	Practical	72	40	100
			Sessional	80	40	100
Total (if passed)				1205		1600
(Theory, Sessional & Practical/Lab / Seminar)						

**RESULT**

(Pass - Total Marks Obtained (in words) )

ONE TWO ZERO FIVE ONLY

1. No. of papers qualified name-

TWO

2. No. of papers previously qualified-

TEN

3. Total No. of papers qualified up-to-date-

TWELVE

Signature

Date of Issue

( Contact no) 27-003211

Checked & Verified by

Controller of Examinations

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Prepared by