



Sl. No. M13 176435

14/11/2013

Date :

STATEMENT OF MARKS

M.Tech in Electronics & Communication
Fourth Semester Examination - January - 2013.

Name: **BIRENDRA KUMAR YADAV**

Reg.No: **111/107/F/MTEC0055**

Paper Code	Paper / Course Title	Credits	Term End Examination		Internal Assessment		Total			Result / Remarks
			Max Marks	Marks Obt	Max Marks	Marks Obt	Max Marks	Min for Pass	Marks Obt	
MSVD01	Embedded System Design	4	80	62	20	18	100	40	80	PASS
MSVD02	Digital VLSI Design	4	80	59	20	19	100	40	78	PASS
MSVD03	Verilog Hardware Description Language	4	80	52	20	17	100	40	69	PASS
MSVD04	Project	4	80	57	20	18	100	40	75	PASS
Grand Total		16					400		302	
THREE HUNDRED TWO										
Grand total (in words)..... only										
Grade System	I Sem Total	II Sem Total	III Sem Total	IV Sem Total	Grand Total	Percentage	Class			
Grade Awarded	363	371	379	302	1415	74.47	I CLASS			

Note : 1) Minima for Pass : 32 marks per course in theory and aggregate 40% including Internal Assessment

2) Course Exemption : 40 marks and above in each course.

3) **Class** : Distinction - 75% & above; First Class - 60% & above but less than 75%; Second Class - 50% & above but less than 65%; Third Class - 35% & above less than 50%; Fail - below 35%

Entered by:

Name: **IEM**

Verified by:

Signature:
Name: **H.C. Lokecha**

Scrutinized by:

Signature:

Name: **VIGNAN N. RAJ**

REGISTRAR (Evaluation)