



# KARNATAKA STATE OPEN UNIVERSITY

Mukthahangotri, Mysore - 570 006

SL. No. M 140117288

## STATEMENT OF MARKS

Date :

30/12/2014

### M.TECH IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOURTH SEMESTER EXAMINATION - AUGUST 2014

315

Name : RAJESH KUMAR SINGH			Reg. No: 111072MTEC0047								
Paper Code	Paper / Course Title	Credits	Term End Examination		Internal Assessment		Total			Result / Remark	
			Max Marks	Marks Obt.	Max Marks	Marks Obt.	Max Marks	Min for Pass	Marks Obt.		
HSVD01	Embedded System Design	4	80	50	20	18	100	40	68	PASS	
HSVD02	Digital VLSI Design	4	80	48	20	19	100	40	67	PASS	
HSVD03	Verilog Hardware Description Language	4	80	52	20	18	100	40	70	PASS	
HSVD04P	Project	12	80	47	20	18	100	40	65	PASS	
Grand Total		24					400		270		
Total Credits Earned :		24									
Semester	I Sem	II Sem	III Sem	IV Sem	Grand Total	Percentage	Quality Level	Class			
Total	390	382	368	270	1410	74.21	Very Good	First Class			
Grand Total in Words:		ONE THOUSAND FOUR HUNDRED TEN									

- Note:**
1. Minima for pass: 32 marks per course in theory and aggregate 40% including Internal Assessment.
  2. Course Exemption: 40 marks and above in each course.
  3. Class: Distinction - 75% & above; First Class - 60% & above but less than 74.99%; Second Class - 50% & above but less than 59.99%; Third Class - 40% & above but less than 49.99%; Fail - Below 40%.

Entered by  
Institute of Engineering & Management



Verified by  
Signature: \_\_\_\_\_  
Name: H.C. LOKESHA

Scrutinized by  
Signature: \_\_\_\_\_  
Name: SHOBHA.H

*[Signature]*  
Registrar (Evaluation)

Registrar (Evaluation)