

0205285

Guru Gobind Singh Indraprastha University, Delhi

CONSOLIDATED STATEMENT OF MARKS

MASTER OF TECHNOLOGY (VLSI DESIGN)



NAME: PRIYANKA YADAV
 ENROLLMENT: 15111204
 FATHER'S NAME: DEEPAK SINGH YADAV
 YEAR OF ADMISSION: 2009
 UNIVERSITY SCHOOL/INSTITUTE: IC-DAC, NOIDA

TOTAL CREDIT OF PROGRAMME: 100
 MINIMUM CREDITS REQUIRED: 100
 YEAR OF COMPLETION: MAY, 2010
 PROGRAMME DURATION: 24 SEMESTERS

COURSE	PAPER	CS	INT	EXT	TOTAL	COURSE	PAPER	CS	INT	EXT	TOTAL						
FIRST SEMESTER																	
ITV001	ADVANCED COMPUTER ARCHITECTURE	3	18	37	58	ITV004	VLSI TECHNOLOGY	3	19	45	77						
ITV005	MOS CIRCUIT DESIGN	3	28	37	68	ITV007	SOLID STATE ELECTRONIC DEVICE	3	18	48	71						
ITV009	DIGITAL SYSTEM DESIGN	3	30	35	68	ITV011	DATA STRUCTURE & ALGORITHMS ANALYSIS	3	18	34	55						
ITV001	LAB - I	3	16	33	52	ITV002	LAB - II	3	34	44	71						
ITV005	LAB - III	3	16	48	71	SECOND SEMESTER											
ITV002	ANALOG VLSI DESIGN	3	17	48	72	ITV004	COMPUTATIONAL TECHNIQUE	3	21	48	69						
ITV006	DIGITAL SIGNAL PROCESSING	3	23	39	72	ITV012	LOW POWER VLSI DESIGN	3	21	48	69						
ITV009	EMBEDDED SYSTEM DESIGN	3	21	30	61	ITV002	LAB - IV	2	33	44	79						
ITV004	LAB - IV	3	33	47	83	ITV006	SEMI-PROJECT I	4	-	65	69						
THIRD SEMESTER																	
ITV011	ALGORITHMS FOR VLSI DESIGN AUTOMATION	3	26	28	61	ITV011	CRYPTOLOGY AND CRYPTO CHIP DESIGN	3	18	35	71						
ITV021	DESIGN OF VLSI SYSTEM	3	20	52	65	ITV011	LAB - VI	3	38	58	85						
ITV010	LAB - VI	2	11	48	76	ITV006	SEMINAR	2	-	78	78						
ITV007	SEMI-PROJECT - II	3	18	33	61	FOURTH SEMESTER											
ITV012	DISSERTATION	20	38	50	88	ITV014	SUMMARY & PROGRESS REPORT	3	-	77	77						
ITV006	COMPREHENSIVE TEST	1	-	73	74	CREDITS EARNED: 100.00 CREDITS ACCOUNTED FOR: 100.00 CPI: 75.75 DIVISION: FIRST WITH DISTINCTION											

CSDM: 100000004

Date of Print: 22-Sep-2010

: With Grace marks Ab: Absent INT: Internal EXT: External CS: Credit Secured

