



# UTTAR PRADESH TECHNICAL UNIVERSITY

LUCKNOW, UTTAR PRADESH (INDIA)

## STATEMENT OF MARKS

### FINAL YEAR

MASTER OF TECHNOLOGY - MICRO ELECTRONICS (VLSI DESIGN)

( SEQUENTIAL PROGRAMME ) 2011

Name of Candidate : UPDESH KUMAR YADAV  
Father's Name : CHHUTTAN LAL YADAV  
Name of Institution : STUDY CENTRE : GALGOTIA'S COLLEGE OF ENGG & TECHNOLOGY, GREATER NOIDA

Roll No. : 6409794028  
Enrolment No. : 640979400132

THIRD SEMESTER		FOURTH SEMESTER (DATE OF COMPLETION): 02.08.2011	
SUBJECT CODE AND NAME	GRADE	SUBJECT CODE AND NAME	GRADE
VLS931 R.F. MICRO-ELECTRONICS	B	DISSERTATION	S
VLS932 MIXED SIGNAL DESIGN	A		
VLS933 TESTING & TESTABILITY	A		
VLS110 IMAGE PROCESSING ALGORITHMS & ARCH.	B		
III SEMESTER PERFORMANCE INDEX(SPI)-	9.00		
II SEMESTER PERFORMANCE INDEX(SPI)-	6.00	CUMMULATIVE PERFORMANCE INDEX (CPI)-	7.67
I SEMESTER PERFORMANCE INDEX (SPI)-	8.00		

#### 1 GRADE POINT

A - 10  
B - 08  
C - 06  
D - 04  
E - 02  
F - 00  
I - INC

#### 2 GRADE FOR DISSERTATION

S SATISFACTORY  
X UNSATISFACTORY

#### 3 MAXIMUM POINGS

SPI 10  
CPI 10

CHECKED BY:

CONTROLLER OF EXAMINATIONS