

## CURRICULUM VITAE

**ANUJ PALIWAL.**

(Electrical & Electronics Engineer)

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### CAREER OBJECTIVEAL

To secure a challenging position in the production technology industries where I can effectively contribute my skills as an "Electrical Engineering" profession possessing completing technical skills.

### ACADEMIC CREDENTIALS

<u>Course</u>	<u>Board/University</u>	<u>College</u>	<u>Duration/Year of Passing</u>	<u>Percentage</u>
Bachelor of Technology (Electrical And Electronics Engineering)	Lovely Professional University	Lovely Institute Of Technology & Science	2015	7.40
Senior Secondary Certificate Examination	CBSE Board	S.S.K.L.A.M. School, Jalaun	2010	50%
Matriculation	CBSE Board	Maharishi Vidya Mandir School, Orai	2008	61%

Competitive Exam - GATE Qualified

### TECHNICAL QUALIFICATION / CERTIFICATION

- Pro. languages : C , C++, Assembly (mp8085).
- Software : Matlab, PSpice.
- Other tools : Microsoft Office..

### INTERNSHIP

- Organisation : MPIL MUSKAAN POWER INFRASTRUCTURE LIMITED  
Ludhiana, Punjab (4<sup>th</sup> June-23<sup>rd</sup> July 2014).
- Objective : To Be Familiar With Design of Transformer.
- Description : Worked as Design Engineer in Electrical department.