



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
HYDERABAD - 500 085, TELANGANA STATE, INDIA.



**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

IT Tech. CONTROL SYSTEMS

CVM. No. : **C 0666775**

Serial No. : 20224011773

Name : **SHAHENOOR SIDDIQUE**



Name of the College : R E C, SHAYAMPET-JAGIR

Month & Year of Final Exam : December, 2015

Hall Ticket No. : **13871D7514**

Year of Admission : 2013-2014

Class Awarded : **FIRST CLASS WITH DISTINCTION**

S.No	SUBJECT TITLE	BT	ET	Total	Pass/Fail
		Marks	Marks	Marks	
	Maximum Marks in Theory	40	60	100	
	Maximum Marks in Lab	40	60	100	

**I SEMESTER**

1	ADVANCED CONTROL SYSTEMS	38	47	85	P
2	DIGITAL CONTROL SYSTEMS	37	29	66	P
3	STATE AND PARAMETER ESTIMATION THEORY	38	53	91	P
4	DSP PROCESSOR ARCHITECTURE AND APPLICATIONS	32	47	79	P
5	ADVANCED INSTRUMENTATION SYSTEMS	36	44	80	P
6	ADVANCED MICROPROCESSORS	36	41	77	P
7	CONTROL ENGINEERING AND SIMULATION LAB	39	58	97	P
8	SEMINAR	48	-	48	P

**II SEMESTER**

1	OPTIMAL CONTROL THEORY	37	26	63	P
2	ADAPTIVE CONTROL THEORY	36	35	71	P
3	NEURAL NETWORKS & FUZZY SYSTEMS	36	44	80	P
4	CONTROL SYSTEM DESIGN	39	38	77	P
5	DISTRIBUTED CONTROL SYSTEMS	36	36	72	P
6	ADVANCED DIGITAL SIGNAL PROCESSING	37	32	69	P
7	SIGNAL PROCESSING LAB	36	58	94	P
8	SEMINAR	48	-	48	P

**III SEMESTER**

1	COMPREHENSIVE VIVA	-	97	97	P
(Comprehensive VIVA External= 100)					

**IV SEMESTER**

1	PROJECT WORK*	-	C	-	P
(* A - Excellent, B - Good, C - Satisfactory, D - Un Satisfactory)					

Aggregate Marks Secured : **294 OUT OF 1600 (80.68%)**

Date of Issue : **January 29, 2016**

(see overleaf for Rules concerned to award of class)



(\*Courses registered but not counted for calculation of aggregate)

*Avesh Kumar Singh*

**CONTROLLER OF EXAMINATIONS**