

BANASTHALI UNIVERSITY

Copy of Marks obtained at the Master of Technology Examination, 2021

Subject : VLSI DesignRoll No. 11479Enrollment No. 2009/1298Name Vand GuptaFather's Name Shri Shiv Kumar Gupta

Course No.	Course Offered	Maximum Marks	Minimum Pass Marks	Marks obtained	Remarks
	Third and Fourth Semester Examination				
	Reading Elective				
RE-01	High Level Synthesis	40		34	
RE-02	VLSI Testing and Design for Testability	40		39	
	Project				
	Part-I				
I	Timely Submission of Proforma	20			
II	Quality of Work	40	100	79	
III	Advisors Evaluation from their Respective Guide	50			
	Part-II				
I	Content	25			
II	Presentation	50	100	71	
III	Project Report	25			
	Part-III				
I	Synopsis	100		79	
	Part-IV				
I	Discussion and Report	100			
II	Interim Report	50	100	146	
III	Seminar	75			
IV	Year Meet	100			
	Total Marks	700	380	534	Pass/Fail

**MASTER OF TECHNOLOGY EXAMINATION
SEMESTERWISE TOTAL MARKS OBTAINED**

First Semester	Second Semester	Third and Fourth Semester	Grand Total	Division
350/450	345/430	534/700	1229/1580	First

C) R - Eligible to reappear

X - Failed

I - Improvement of performance. The candidate reappearing in the paper(s) in the year given in bracket and marks shown are those obtained on reappearance.

D - Candidate did not clear the paper(s) with main examination and cleared the same in the examination in the year given in bracket and marks shown are those obtained on reappearance.

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Written by LeelaResult Declared on 08/04/2021Checked by P. Sharma

OFFICIAL SECRETARY